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Attorney Docket No.: FILLF1.001CP1 Date: March 6, 2002

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TC 2800 MAIL ROOM REQUEST FOR CONTINUED EXAMINATION (RCE)

UNDER 37 C.F.R. § 1.114

Application Number:

09/460,630

Filing Date:

December 14, 1999

First Named Inventor:

Bart Dierickx

Group Art Unit:

2815

Examiner Name:

Eugene Lee

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: United States Patent and Trademark Office, P.O. Box 2327, Box RCE, Arlington, VA 22202, on

March 6, 2002

United States Patent and Trademark Office P.O. Box 2327 Box RCE Arlington, VA 22202

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

- 1. Submission Required under 37 C.F.R. § 1.114:
 - (X) Enclosed:

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(X) Amendment 01 FC:179

740.00 OP

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Stephen C. Jensen

Registration No. 35

Attorney of Record

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant Bart Dierickx Group Art Unit 2815 I hereby certify that this correspondence and all 09/460,630 Appl. No. marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Assistant Filed December 14, 1999 Commissioner for Patents, Washington, D.C. 20231, on For BURIED, FULLY DEPLETABLE, HIGH FILL **FACTOR PHOTODIODES** Stephen C. Jensen, R. S. No. 35,556 Examiner Eugene Lee **AMENDMENT** Assistant Commissioner for Patents Washington, D.C. 20231

In response to the Office Action mailed December 6, 2001, please amend the above-captioned patent application as follows:

IN THE CLAIMS

Dear Sir:

Please amend Claim 1 as follows:

1. (Twice Amended) An active or passive pixel structure comprising:

a semiconductor substrate with dopants of a first conductivity type at a first concentration density, and with an insulating layer at its surface;

a collection region with dopants of a second conductivity type which is opposite the first conductivity type at a second concentration density, formed in the surface region of the semiconductor substrate;

a dual-purpose electrode formed on the insulating layer, extending over both the surface of at least part of the collection region and over at least part of the substrate, the dual-purpose electrode being intended to be driven by a first voltage that causes an electrostatic potential which collects in an area of the collection region beneath the dual-purpose electrode charges generated by electromagnetic radiation and by a second